

Docket No. 214391US-2S

IN RE APPLICATION OF: Hidetaka HATTORI

SERIAL NO: 09/961,284 248 P

FILED: SEPTEMBER 25, 2001

FOR: POWER SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an **Amendment w/ Marked-up Copy** in the above-identified application.

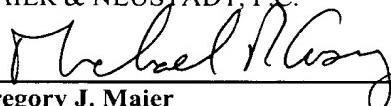
- No additional fee is required**
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	20	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	2	MINUS	3	0	× \$80 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS				+ \$270 =		\$0.00
TOTAL OF ABOVE CALCULATIONS						\$0.00
<input type="checkbox"/> Reduction by 50% for filing by Small Entity						\$0.00
<input type="checkbox"/> Recordation of Assignment				+ \$40 =		\$0.00
					TOTAL	\$0.00

- A check in the amount of _____ is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

HIDETAKA HATTORI

SERIAL NO: 09/961-284
09/961-248 Pw)

FILED: SEPTEMBER 25, 2001

FOR: POWER SEMICONDUCTOR
DEVICE AND MANUFACTURING
METHOD THEREOF

: EXAMINER: FARAHANI, D.

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: GROUP ART UNIT: 2814

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AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

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SIR:

In response to the Office Action dated February 19, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend Claims 1-11 as shown in clean form below.¹

1. (Amended) A power semiconductor device comprising:
 - a base layer of a first conductivity type;
 - a base layer of a second conductivity type selectively formed on one surface of said layer of the first conductivity type;

¹A marked-up copy of the amended portion of the claims is attached hereto.